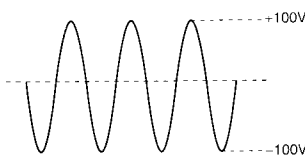




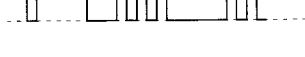
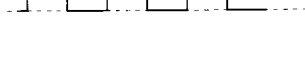




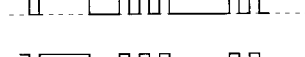
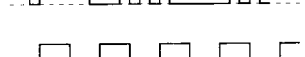



TP	Name	Signal	Freq/Per	Description
442	BACKLIGHT		1 kHz	Backlight supply voltage (backlight ON)
446	DTCLA		500 kHz	Data clock for LCD row drivers
450	LCD0		varying	LCD row driver data input
451	LCD1		varying	LCD row driver data input
452	LCD2		varying	LCD row driver data input
453	LCD3		varying	LCD row driver data input
454	M		35Hz	LCD backplane modulation signal
455	LINECL		16 kHz	LCD column clock; pulse length 2 μs
456	DATACL		0.8 ... 1.2 μs	LCD row driver data clock
460	LCD0A		varying	LCD row driver data input
461	LCD1A		varying	LCD row driver data input
462	LCD2A		varying	LCD row driver data input
463	LCD3A		varying	LCD row driver data input
464	M1		600Hz	Randomized backplane modulation signal
501	ON	+5V -5.5V -5V	DC DC DC	At power on; At power off, if mains adapter voltage is present At power off, battery supply only